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DATE: $7 - 3 - 00$	6
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SUBJECT : Request for Certificate of C	Correction on Patent No.: 696////
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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

: 6,961,111

APPLICATION NO .: 09/831,315

ISSUE DATE

: November 1, 2005

INVENTOR(S)

Keizaburo KURAMASU, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the front page, column 1, section (75), add inventor --Yutaka Nanno, Takarazuka (JP)--.

On the front page, column 2, section (56), add Other Documents subsection, add citation --Inoue et al. "4.2:

Low-Temperature Poly-Si TFT-LCD with Integrated 6-bit Digital Data Drivers," 1996, pages 21-24--.

In the claims:

in claim 2, column 26, line 16, replace "comprising" with --comprises--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gethering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any commercia on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Petent and Trademark Office, U.S. Department of Commerce, P.O. 80x 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. 80x 1450, Alexandria, VA 22313-1450.

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